

Programmable Logic Device With A Memory-Based Finite State Machine

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1/3

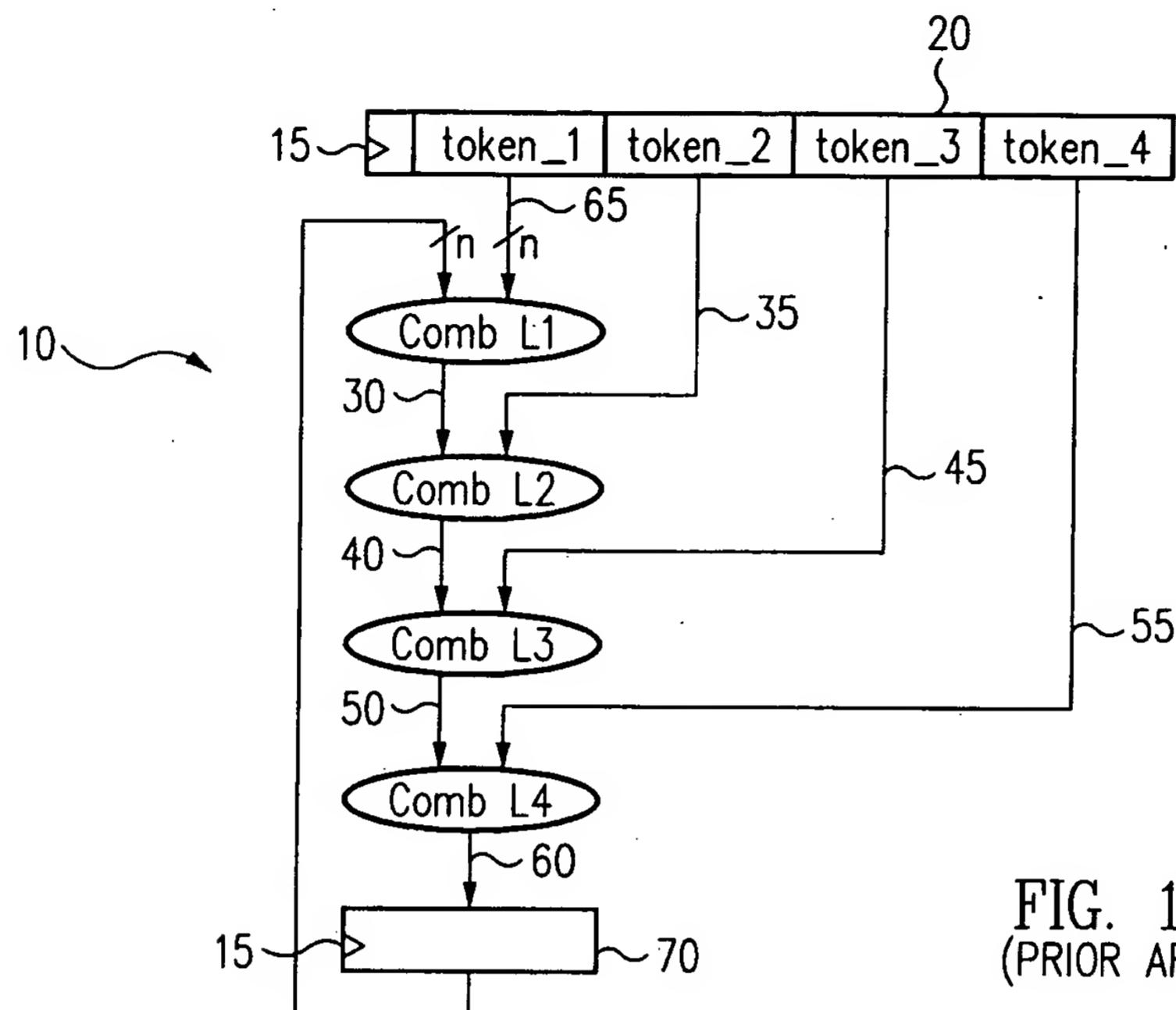


FIG. 1a  
(PRIOR ART)

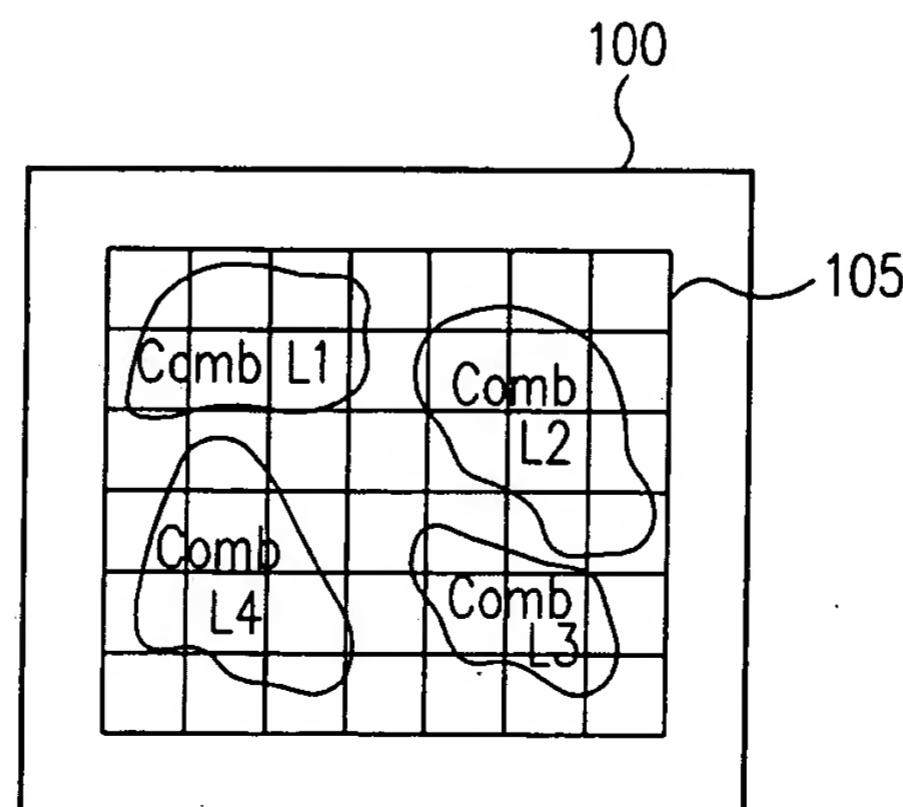


FIG. 1b  
(PRIOR ART)

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2/3

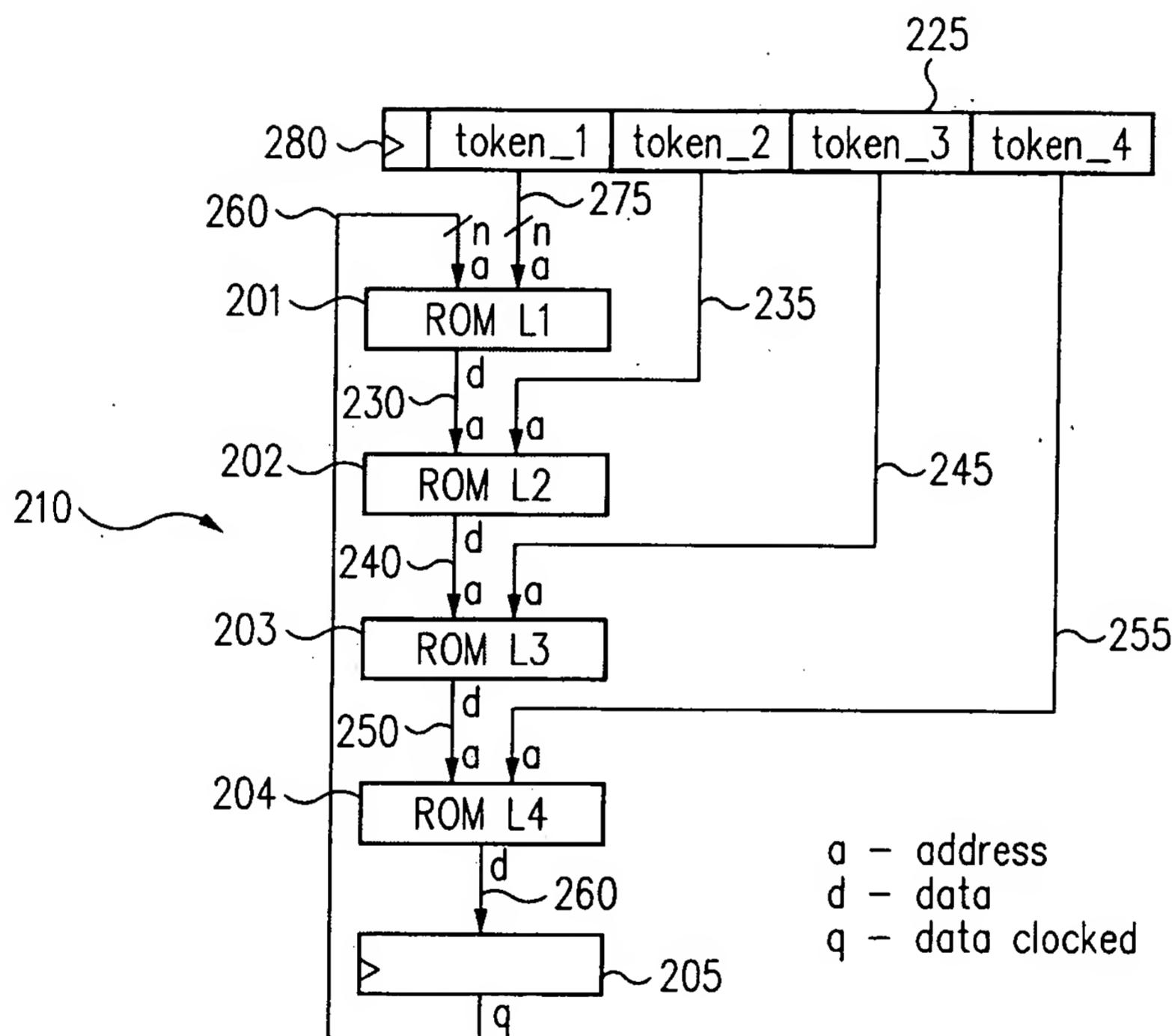


FIG. 2  
(PRIOR ART)

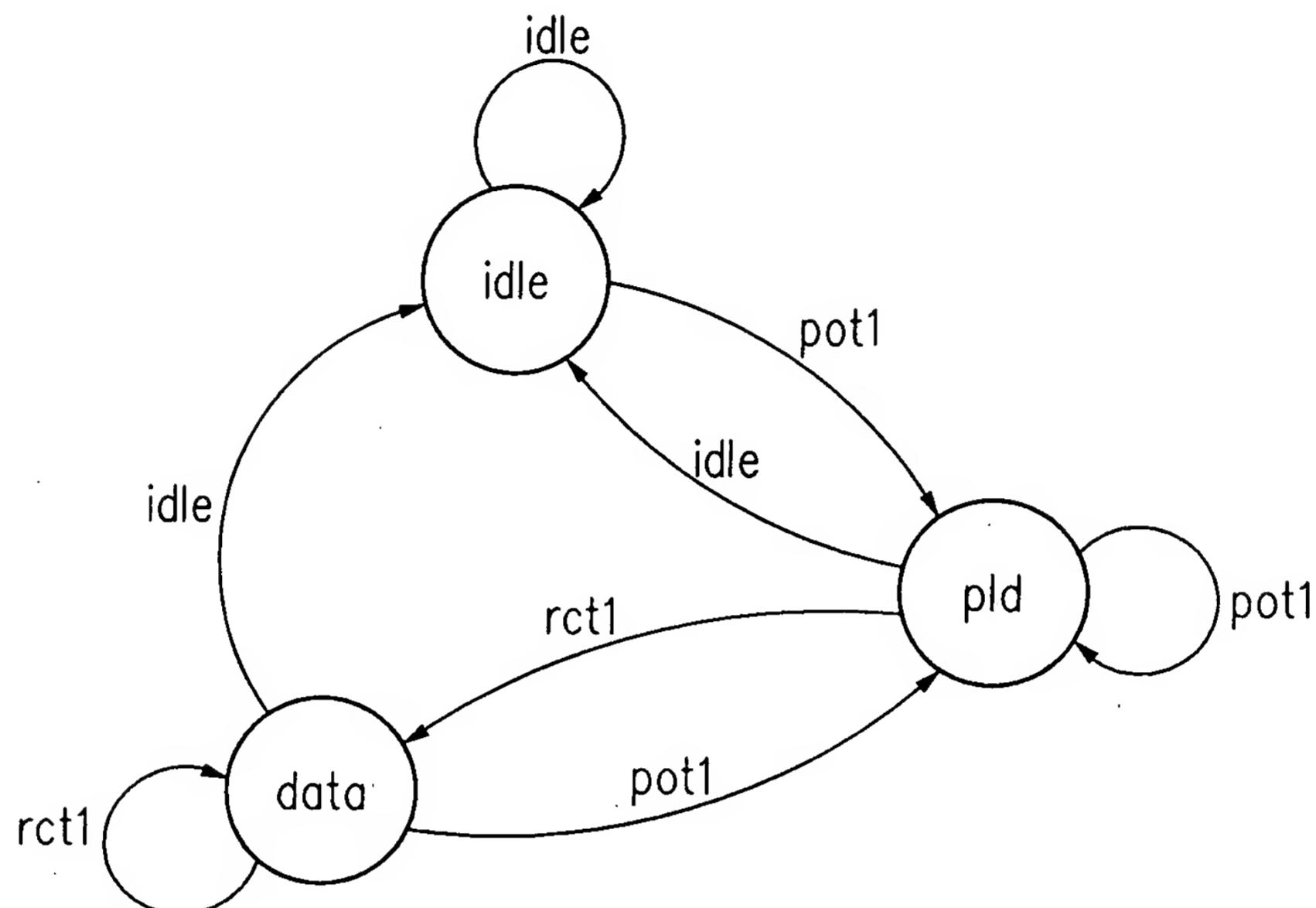


FIG. 3

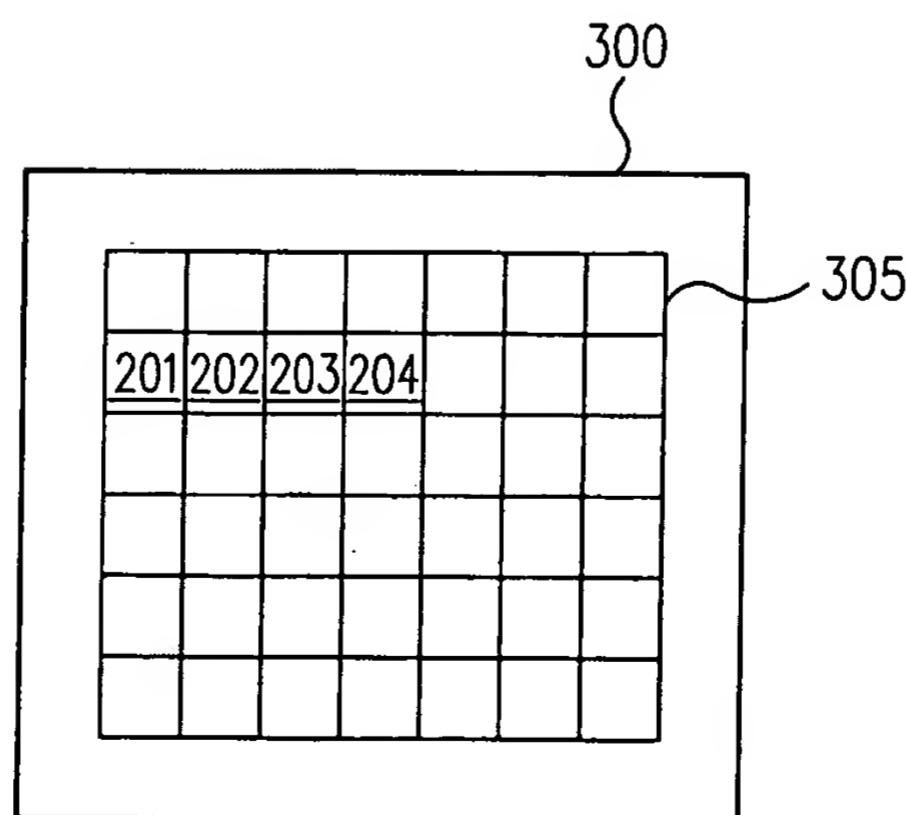


FIG. 4